



MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE

(UGC-AUTONOMOUS INSTITUTION)

Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi

NAAC Accredited with A+ Grade, NIRF India Rankings 2025 - Band: 201-300 (Engg.)

NBA Accredited - B.Tech. (CIVIL, CSE,CST, ECE, EEE, MECH), MBA & MCA



Examination Branch

M. Tech II Year I Semester (R24) Supplementary End Semester Examinations, June-2026
(2024 Admitted batch)

TIME TABLE

ACADEMIC YEAR: 2025-26

Time: 10:00 AM to 01:00 PM

Duration: 3 hours

Date/Day	Computer Science and Engineering (CSE)	VLSI Design and Embedded Systems
15.06.2026 (Monday)	Big Data Computing-24CSEP4M01	VLSI Interconnects-24VESP4M01
	Introduction to Industry 4.0 and Industrial Internet of Things-24CSEP4M02	C-Based VLSI Design-24VESP4M02
	Data Science for Engineers-24CSEP4M03	Multi-Core Computer Architecture-24VESP4M03
	Blockchain and its Applications-24CSEP4M04	
16.06.2026 (Tuesday)	Automation and Robotics-24MEP302	

Contaror G. Muris

CONTROLLER OF EXAMINATIONS

Date: 02.06.2026

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
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1. CSE HOD ECE HOD

2. Transport Incharge

P. Ranga

PRINCIPAL

Principal

Madanapalle Institute of

Technology & Science

3. Notice Board (AS & Dept)

4. File (AS)